Antenna Performance

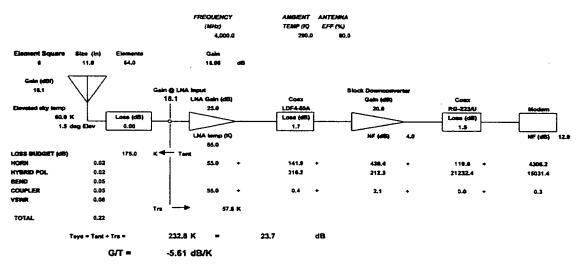
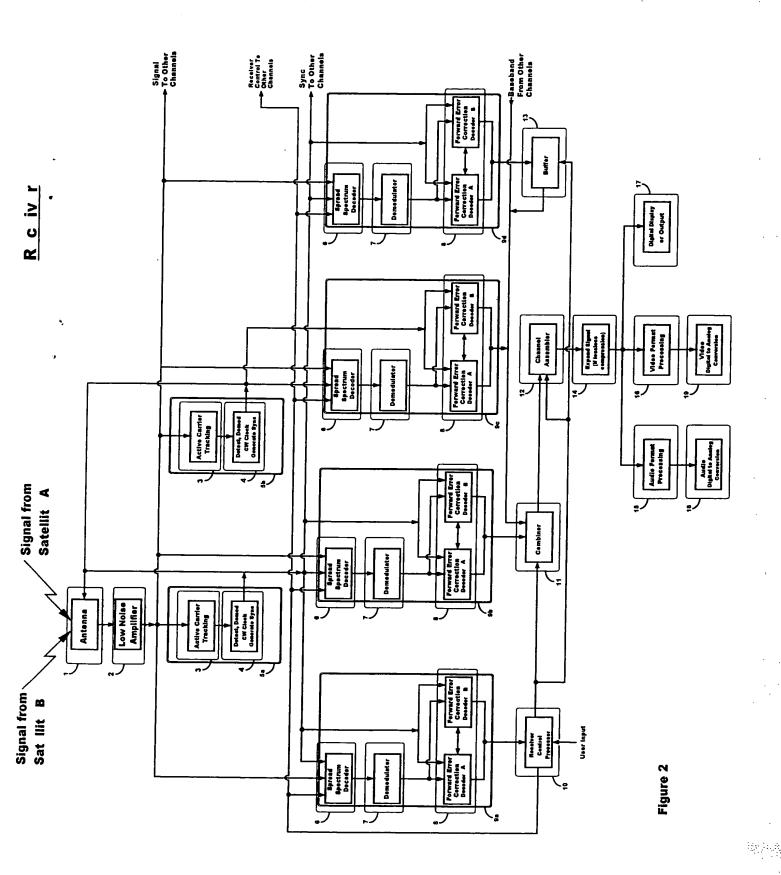
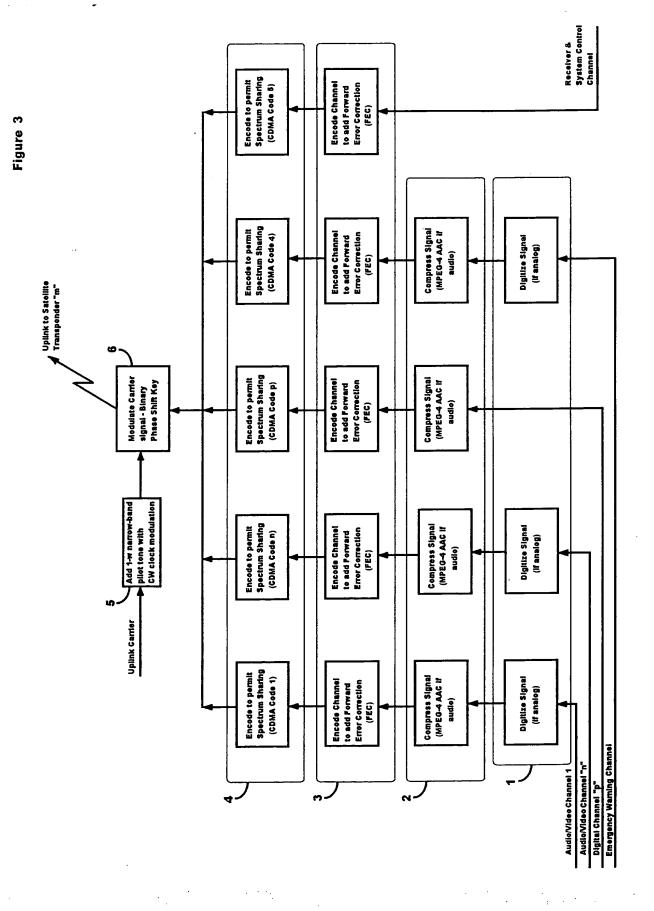


Figure 1 8x8 Phased Array Antenna



Uplink Proc ss ř · · (Elements may not be physically colocat d)



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100	1		Preferred Design for Audio Applications
orep	Where	Action	Signal Process
-	Audio Processor	Digitize Audio Source	Sampling to 22.05-kHz audio @ Nyoniist rate (44.1 kHz and)
7	Audio Processor		Quantization: 16 bits per sample (65,536 levels)
ო	Audio Processor	Compress Audio	MPEG-4 Advanced Audio coding (AAC) (incorporates Huffman coding, Unequal Error Protection (UEP)) 24-kbps per high quality (4.2 on scale of 5) music channel
4	Audio Processor	(Baseband)	Use adaptive transform Fractionary Domain Cadian 8
2	Audio Processor	Add Speech Channels	Narrowband Code Excited Linear Prediction (CELIP) coding 6 to 6 the
ဖ		Add Non-audio Digital Channels	Variable size channels (rates) carrying broadcast digital data
_		Add Receiver Control Channel	24-kbps channel allocated per transponder for control information
ω	Uplink Processor	Channel coding	Forward Error Correction (FEC); Recursive, Systematic, Convolutional (RSC) Turbo Code, Rate 1/4, length 15, design for 10-5 BER, Parallel Concatenated Convolutional Codes (PCC)
6	Uplink Processor		Use punctured convolutional coding to permit Equal Error Protection (EEB) § Hocorrol Error Protection
2	Uplink Processor		Uplink block length dynamically adapted
Ξ	Uplink Processor		Direct Sequence Spread Spectrum Code Division Multiple Access (DSSS CDMA)
12	Uplink Processor		Combine with other channels - Number of channels datermined by Set EIDB 11500 Action 6:
13	Uplink Processor		Modulate Uplink Carrier - Binary Phase Shift Key (BPSK)
4	Uplink Processor		Add Pilot Tone (center freg)
13	Uplink Transmitter Uplink	Uplink	Transmit to Spacecraft
	S/C Transponder	Receive, Turnaround	Spacecraft Transponder Timazound
П	S/C Transponder		37 - 42 dBw EIRP
∞	Receiver Antenna	Receive Signal	Receiver Antenna Receive pilot fone, phase antenna
19	Receiver Antenna	Detect, Synchronize Carrier Signal	Detect Signal Phase, Synchronize receiver clock Active Carrier Tracking Carrier Tracking Carrier
20	Receiver Antenna		Downconvert signal to 70 MHz IF
22	Receiver	Bit Synchronization	
8	Receiver	Decode Signal	Maximum a Posteriori algorithm (MAP) decoding
ļ	Receiver		Detect desired channel-specific CDMA code
75	Receiver		Use Backward Adaptive Bit Allocation
- 1	Receiver		Reconstruct compressed audio signal, Expand to uncompressed audio
92	Receiver		Digital to Analogue conversion
- 1	Receiver		Feed playback system
82			Verify authorization to receive desired channel Identify, Intelligit in 1
	Receiver	Auxillary Functions	Strip and display andillary channel information. Determine if Warming Channel should are small
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